

WHAT IS CLAIMED IS:

- 1 1. An apparatus for processing semiconductor substrates, the apparatus
2 comprising:
3 a chamber defining a processing region therein;
4 a substrate support disposed in the chamber to support a semiconductor
5 substrate;
6 at least one nozzle extending into the chamber to introduce a process gas into
7 the chamber through a nozzle opening; and
8 at least one heat shield, each heat shield disposed around at least a portion of
9 one of the at least one nozzle, the heat shield having an extension which projects distally of
10 the nozzle opening of the nozzle and which includes a heat shield opening for the process gas
11 to flow therethrough from the nozzle opening.
- 1 2. The apparatus of claim 1 wherein the heat shield comprises a ceramic
2 material.
- 1 3. The apparatus of claim 1 wherein the heat shield comprises a material
2 selected from the group consisting of aluminum oxide, aluminum nitride, and silicon carbide.
- 1 4. The apparatus of claim 1 wherein the extension of the heat shield
2 projects distally of the nozzle opening by a distance of between about a radius of the nozzle
3 and about a diameter of the nozzle.
- 1 5. The apparatus of claim 1 wherein the heat shield is disposed around
2 substantially the entire nozzle extending inside the chamber.
- 1 6. The apparatus of claim 1 wherein a plurality of nozzles are disposed
2 around the substrate support and each nozzle has a heat shield disposed around at least a
3 portion thereof.
- 1 7. The apparatus of claim 6 wherein the heat shields are disposed around
2 the substrate support and configured such that the heat shield openings of the heat shields are
3 disposed radially outwardly of a periphery of the semiconductor substrate.
- 1 8. The apparatus of claim 1 wherein the heat shield comprises a hollow,
2 cylindrical member.

1 9. A heat shield for shielding a nozzle extending into a chamber to
2 introduce a process gas into the chamber through a nozzle opening, wherein the chamber
3 defines a processing region therein and has a substrate support to support a semiconductor
4 substrate for processing in the chamber, the heat shield comprising:

5 a hollow member configured to be coupled with the nozzle and having an
6 internal dimension sufficiently large to be disposed around at least a portion of the nozzle, the
7 hollow member having an extension which projects distally of the nozzle opening of the
8 nozzle and which includes a heat shield opening for the process gas to flow therethrough
9 from the nozzle opening.

1 10. The heat shield of claim 9 wherein the hollow member is cylindrical
2 and has an internal cross-section which is slightly larger than an external cross-section of the
3 nozzle.

1 11. The heat shield of claim 9 wherein the hollow member comprises a
2 ceramic material.

1 12. The heat shield of claim 9 wherein the extension of the heat shield is
2 sized to project distally of the nozzle opening by a distance of between about a radius of the
3 nozzle and about a diameter of the nozzle.

1 13. A method of processing semiconductor substrates, the method
2 comprising:

3 placing a substrate on a substrate support in a chamber defining a processing
4 region therein;

5 introducing one or more process gases through at least one nozzle which
6 extends into the chamber and which has a nozzle opening for the one or more process gases
7 to flow therethrough;

8 applying energy in the processing region to perform a process on the substrate;
9 and

10 providing a heat shield for the nozzle, the heat shield being disposed around at
11 least a portion of the nozzle to reduce a temperature rise of the nozzle from the process
12 performed on the substrate, the heat shield having an extension which projects distally of the
13 nozzle opening of the nozzle and which includes a heat shield opening for the process gas to
14 flow therethrough from the nozzle opening.

1 14. The method of claim 13 wherein applying energy in the processing
2 region comprises generating a plasma in the processing region.

1 15. The method of claim 14 wherein generating the plasma in the
2 processing region comprises applying a source RF power of at least about 10 kW.

1 16. The method of claim 13 wherein the process performed on the
2 substrate comprises forming a layer from the one or more process gases.

1 17. The method of claim 16 wherein the layer fills a gap in the substrate
2 and the gap has a width of at most about 90 nm.

1 18. The method of claim 16 wherein the layer fills a gap in the substrate
2 and the gap has an aspect ratio of at least about 4.

1 19. The method of claim 13 wherein the extension of the heat shield
2 projects distally of the nozzle opening by a distance of between about a radius of the nozzle
3 and about a diameter of the nozzle.

1 20. The method of claim 13 wherein a plurality of nozzles having heat
2 shields are disposed around the substrate, and wherein the heat shields are disposed around
3 the substrate and configured such that the heat shield openings of the heat shields are
4 disposed radially outwardly of a periphery of the substrate.